

S/N unknown

PATENT 553860

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: NANAI et al.

Examiner: 1620 Rec'd PCT/PTO unknown

20 OCT 2005

Serial No.: Unknown

Group Art Unit: Unknown

Filed: Concurrent herewith

Docket: 10873.1744USWO

Title: FIELD EFFECT TRANSISTOR, ELECTRICAL ELEMENT  
ARRAY, AND MANUFACTURING METHOD FOR THE  
SAME

**CERTIFICATE UNDER 37 CFR 1.10**

Express Mail mailing label number: ED 977673158 US

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I hereby certify that the papers listed below are being deposited with the United States Postal Service Express Mail Post Office to Addressee service under 37 CFR 1.10 in an envelope addressed to: Mail Stop PCT, Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.

By:   
Name: YUHGO MARUTA

**COMMUNICATION REGARDING REQUESTED FIGURE**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

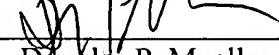
In connection with the above-identified application, applicant respectfully requests that Figure 1A be printed on the front page of the published application.

If a telephone conference would be helpful in resolving any issues concerning this communication, please contact Applicants' primary attorney-of record Douglas P. Mueller (Reg. No. 30,300), at (612) 455.3804.

Respectfully submitted,

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Dated: October 20, 2005

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DPM:nel